

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC07AP, TC74HC07AF

Hex Buffer (open drain)

The TC74HC07A is a high speed CMOS BUFFER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

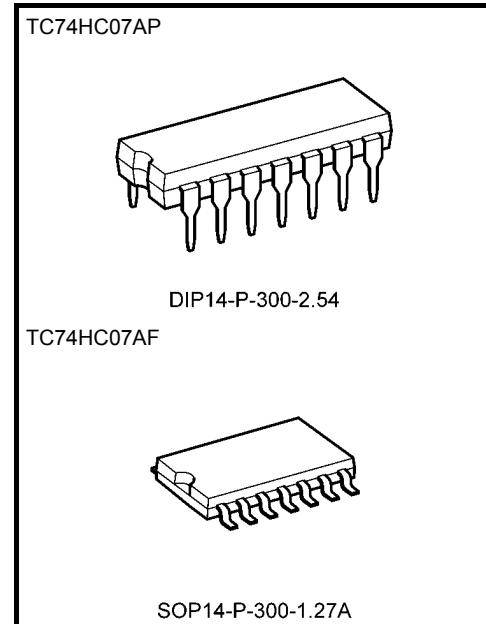
Pin configuration and function are the same as the TC74HCT7007A. But the TC74HC07A has high performance MOS N-channel transistor (open-drain) outputs.

This device can, therefore, with a suitable pull-up resistors, be used in wired-AND, LED driver and other applications.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

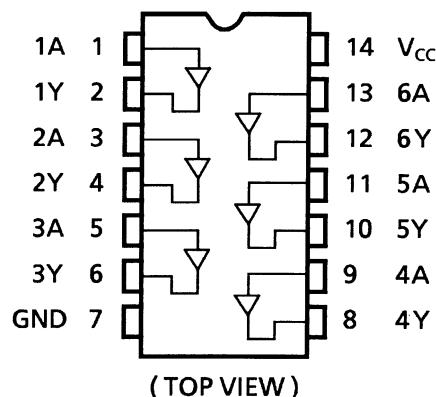
Features

- High speed: $t_{pz} = 5$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 1 \mu A$ (max) at $T_a = 25^\circ C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 10 LSTTL loads
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Open drain structure.
- Pin and function compatible with 74LS07

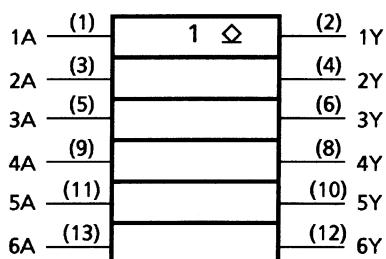


Weight
DIP14-P-300-2.54 : 0.96 g (typ.)
SOP14-P-300-1.27A : 0.18 g (typ.)

Pin Assignment



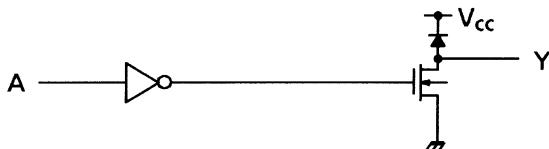
IEC Logic Symbol



Truth Table

A	Y
L	L
H	Z

Z: High impedance

System Diagram (per gate)**Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	I _{OUT}	+25	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	P _D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	t _r , t _f	0 to 1000 (V _{CC} = 2.0 V) 0 to 500 (V _{CC} = 4.5 V) 0 to 400 (V _{CC} = 6.0 V)	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics**DC Characteristics**

Characteristics	Symbol	Test Condition		V _{CC} (V)	Ta = 25°C			Ta = -40 to 85°C		Unit	
				Min	Typ.	Max	Min	Max			
High-level input voltage	V _{IH}	—		2.0	1.50	—	—	1.50	—	V	
				4.5	3.15	—	—	3.15	—		
				6.0	4.20	—	—	4.20	—		
Low-level input voltage	V _{IL}	—		2.0	—	—	0.50	—	0.50	V	
				4.5	—	—	1.35	—	1.35		
				6.0	—	—	1.80	—	1.80		
Low-level output voltage	V _{OL}	V _{IN} = V _{IL}	I _{OL} = 20 µA	2.0	—	0.0	0.1	—	0.1	V	
				4.5	—	0.0	0.1	—	0.1		
				6.0	—	0.0	0.1	—	0.1		
			I _{OL} = 4 mA	4.5	—	0.17	0.26	—	0.33		
				6.0	—	0.18	0.26	—	0.33		
Output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC}		6.0	—	—	±0.5	—	±5.0	µA	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	—	—	±0.1	—	±1.0	µA	
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	—	—	1.0	—	10.0	µA	

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition			Min	Typ.	Max	Unit
Output transition time	t _{THL}	—			—	4	8	ns
Propagation delay time	t _{pLZ}	R _L = 1 kΩ			—	5	15	ns
Propagation delay time	t _{pZL}	R _L = 1 kΩ			—	5	15	ns

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit	
			VCC (V)	Min	Typ.	Max	Min		
Output transition time	t_{THL}	—	2.0	—	30	75	—	95	ns
			4.5	—	8	15	—	19	
			6.0	—	7	13	—	16	
Propagation delay time	t_{pLZ}	$R_L = 1 \text{ k}\Omega$	2.0	—	10	90	—	115	ns
			4.5	—	7	18	—	23	
			6.0	—	6	15	—	20	
Propagation delay time	t_{pZL}	$R_L = 1 \text{ k}\Omega$	2.0	—	17	90	—	115	ns
Input capacitance	C_{IN}	—	—	—	5	10	—	10	pF
Output capacitance	C_{OUT}	—	—	—	3	—	—	—	pF
Power dissipation capacitance	C_{PD} (Note)	—	—	—	4	—	—	—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

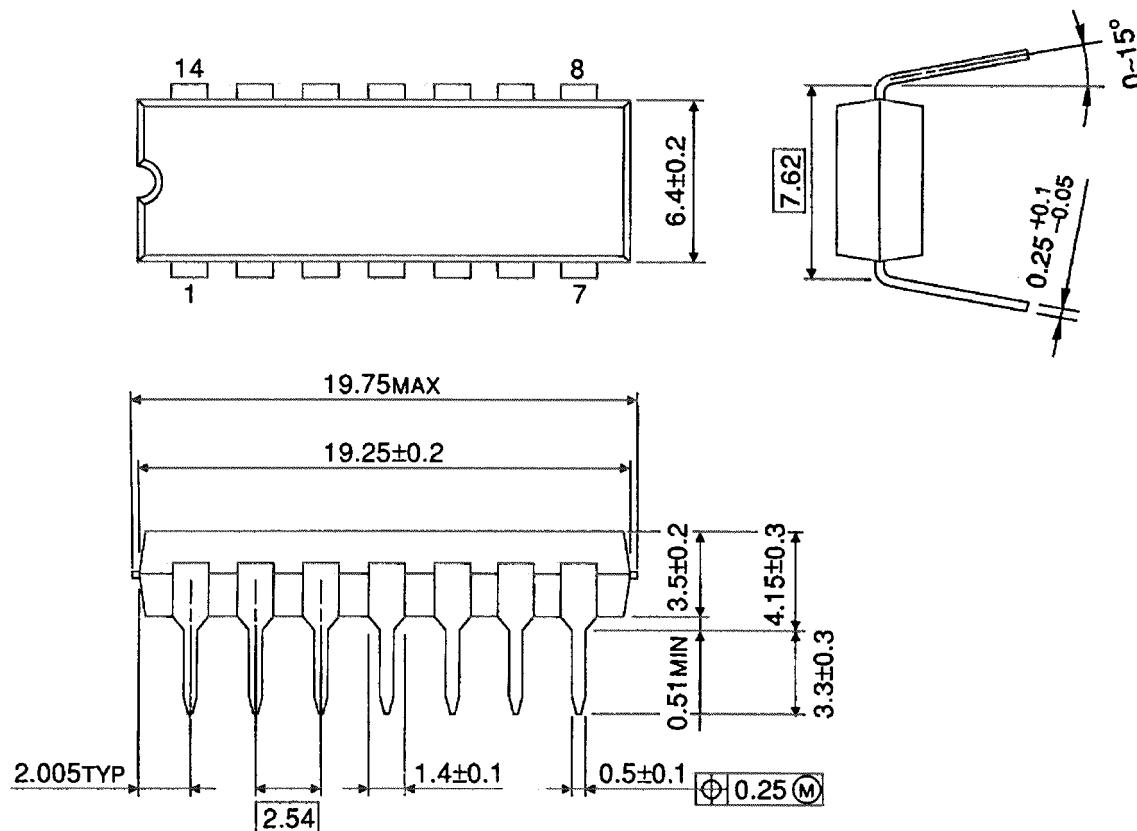
Average operating current can be obtained by the equation:

$$I_{CC} (\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$$

Package Dimensions

DIP14-P-300-2.54

Unit : mm

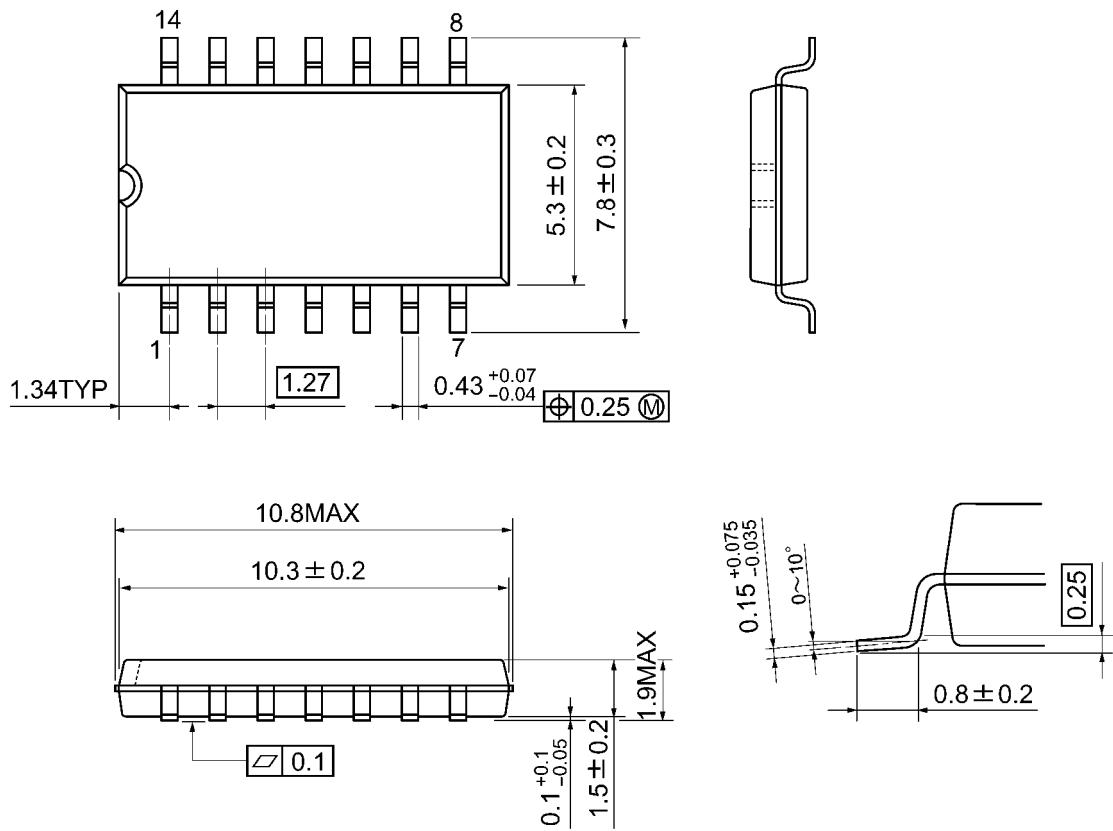


Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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